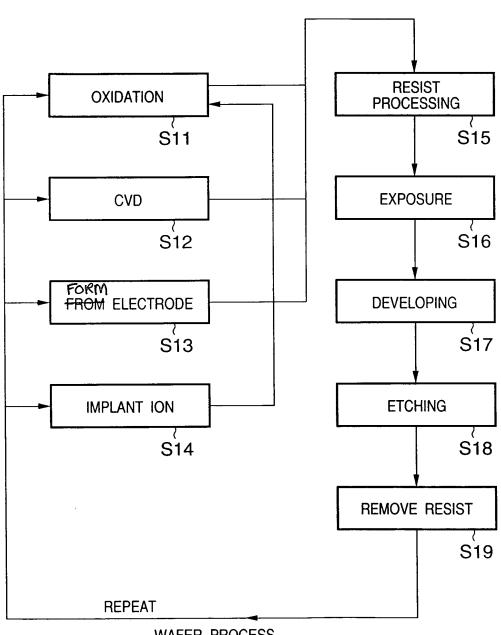


FIG. 13



WAFER PROCESS